



Form 249 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty Docket No. NOVLP098 Applicant: Wu et al. Filing Date 03-11-2004	Application No.: 10/800,409 Group 1762
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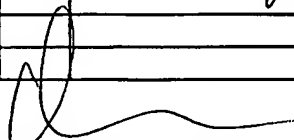
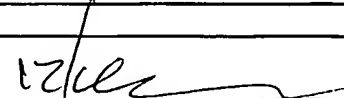
U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	C1	U.S. Office Action mailed August 24, 2005, from U.S. Application No. 10/404,693 [Atty Dkt No. NOVLP064/NVLS-794].
	C2	U.S. Office Action mailed September 1, 2005, from U.S. Application No. 10/672,305 [Atty Dkt No. NOVLP069/NVLS-000821].
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U.S. Patent Documents

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Foreign Patent or Published Foreign Patent Application

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Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	C1	U.S. Office Action mailed July 13, 2005, from U.S. Application No. 10/672,311 [Atty Dkt No. NOVLP075/NVLS-000820].
	C2	U.S. Office Action mailed July 27, 2005, from U.S. Application No. 10/785,235 [Atty Dkt No. NOVLP085/NVLS-2875].
		NOT A PUBLICATION
Examiner		Date Considered

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Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	<table style="width: 100%;"> <tr> <td style="width: 50%;"> Atty Docket No. NOVLP098 Applicant: Wu et al. Filing Date 03-11-2004 </td> <td style="width: 50%;"> Application No.: 10/800,409 Group 1762 </td> </tr> </table>	Atty Docket No. NOVLP098 Applicant: Wu et al. Filing Date 03-11-2004	Application No.: 10/800,409 Group 1762
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Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A1	6,329,017	12.11.01	Liu et al.			
	A2	6,383,466	05.07.02	Domansky et al.			
	A3	6,365,266	04.02.02	MacDougall et al.			
	A4	5,504,042	04.02.96	Cho et al.			
	A5	5,858,457	01.12.96	Brinker et al.			
	A6	6,270,846	08.07.01	Brinker et al.			
	A7	6,387,453	05.14.02	Brinker et al.			
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	A10	5,700,844	12.23.97	Hedrick et al.			
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	A15	5,686,054	11.11.97	Barthel et al.			
	A16	5,851,715	12.22.98	Barthel et al.			
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	A33	2002/0034626	03.21.02	Liu et al.			
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Examiner 	Date Considered 12/19/04						

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Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty Docket No. NOVLP098	Application No.: 10/800,409
	Applicant: Wu et al.	
	Filing Date 03-11-2004	Group 1762

U.S. Patent Documents

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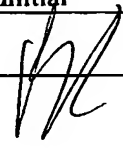

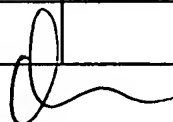
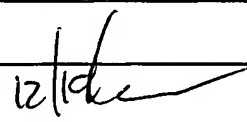
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							Yes	No
V	B1	WO95/07543	03.16.95	WIPO			X	
Examiner				Date Considered				
				12/16				

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Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	C1	Cho et al., "Plasma Treatments of Molecularly Templated Nanoporous Silica Films," <i>Electrochemical and Solid-State Letters</i> , 4 (4) G35-G38 (2001)
	C2	Yung et al., "Spin-on Mesoporous Silica Films with Ultralow Dielectric Constants, Ordered Pore Structures, and Hydrophobic Surfaces," <i>Adv. Mater.</i> 2001, 13, No. 14, 1099-1102
	C3	Schulberg et al., "System for Deposition of Mesoporous Materials," U.S. Patent Application No. 10/295,965, filed November 15, 2002, 64 Pages
	C4	Watkins et al., "Mesoporous Materials and Methods," U.S. Patent Application No. 10/301,013, filed November 21, 2002, 34 Pages
	C5	Justin F. Gaynor, "In-Situ Treatment of Low-K Films With a Silylating Agent After Exposure To Oxidizing Environments," U.S. Patent Application No. 10/056,926 filed January 24, 2002, 34 Pages
	C6	Humayun et al., "Method for Forming Porous Films By Porogen Removal Combined With In SITU Surface Modification", Novellus Corporation, Application No. 10/404,693, filed 3/31/03, pages 1-32. Atty. Docket No. NOVLP064/NVLS-0007
	C7	Tipton et al., "Method Of Porogen Removal From Porous Low-K Films Using UV Radiation", Novellus Systems, Inc., Application No. 10/672,311, filed 9/26/03, pages 1-27. Atty. Docket No. NOVLP075/NVLS-000820
	C8	U.S. Patent Application No. 10/016,017, File Date: December 12, 2001 (Atty Dkt: NOVLP030)
	C9	U.S. Patent Application No. 10/125,614, File Date: April 18, 2002 (Atty Dkt: NOVLP028)
	C10	U.S. Patent Application No. 10/202,987, File Date: July 23, 2002 (Atty Dkt: NOVLP028X1)
	C11	Tipton et al., "Method for Removal of Porogens From Porous Low-K Films Using Supercritical Fluids", Novellus Systems, Inc., Application No. 10/672,305, filed 9/26/03, pages 1-32. Atty. Docket No. NOVLP069/NVLS-000821
	C12	Gangpadhyay et al., "The First International Surface Cleaning Workshop," Northeastern University, November 11-14, 2002
	C13	Cho et al., "Method and Apparatus for UV Exposure of Low Dielectric Constant Materials for Porogen Removal and Improved Mechanical Properties", Novellus Systems, Inc., Application No. 10/800,377, filed 3/11/04, pages 1-31. Atty. Docket No. NOVLP089/NVLS-2887 NOT A PUBLICATION
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	C14	Wu et al., "Method and Apparatus of UV Exposure of Low Dielectric Constant Materials for Porogen Removal and Improved Mechanical Properties", Novellus Systems, Inc., Application No. 10/807,680, filed 3/23/04, pages 1-34. Atty. Docket No. NOVLP097/NVLS-2906
	C15	Humayun et al., "Method For Forming Porous Films By Porogen Removal Combined With In Situ Modification", U.S. Patent No. 10/404,693, filed March 31, 2003, Office Action dated March 15, 2005 (Atty Dkt: NOVLP064)
	C16	Tipton et al., "Method Of Porogen Removal From Porous Low-K Films Using UV Radiation", U.S. Application No. 10/672,311, filed September 26, 2003, Office Action dated September 7, 2004 (Atty Dkt: NOVLP075/NVLS-000820)
	C17	Tipton et al., "Method Of Porogen Removal From Porous Low-K Films Using UV Radiation", U.S. Application No. 10/672,311, filed September 26, 2003, Office Action dated December 28, 2004 (Atty Dkt: NOVLP075/NVLS-000820)
	C18	Tipton et al., "Method For Removal Of Porogens From Porous Low-K Films Using Supercritical Fluids", U.S. Patent No. 10/672,305, Office Action dated March 22, 2005 (Atty Dkt: NOVLP069).
	C19	Bandyopadhyay et al., "Method to Improve Mechanical Strength of Low-K Dielectric Film Using Modulated UV Exposure", U.S. Patent Application No. 10/825,888, filed April 16, 2004 (Atty Dkt: NOVLP088US/NVLS-2882)
B	C20	R.D. Miller et al., "Phase-Separated Inorganic-Organic Hybrids for Microelectronic Applications," MRS Bulletin, October 1997, Pages 44-48
	C21	Jin et al., "Nanoporous Silica as an Ultralow-k Dielectric," MRS Bulletin, October 1997, Pages 39-42
	C22	Asoh et al., "Fabrication of Ideally Ordered Anodic Porous Alumina with 63 nm Hole Periodicity Using Sulfuric Acid," J. Vac. Sci. Technol. B 19(2), Mar/Apr 2001, Pages 569-572
	C23	Asoh et al., "Conditions for Fabrication of Ideally Ordered Anodic Porous Alumina Using Pretextured Al," Journal of the Electrochemical Society, 148 (4) B152-B156 (2001) Pages B152-B156
	C24	Holland et al., "Nonlithographic Technique for the Production of Large Area High Density Gridded Field Sources," J. Vac. Sci. Technol. B 17(2), Mar/Apr. 1999, Pages 580-582
	C25	Masuda et al. "Highly Ordered Nanochannel-Array Architecture in Anodic Alumina," App. Phys. Lett. 71(19), November 1997, Pages 2770-2772
	C26	Clube et al., "White Paper from Holotronic Technologies SA; downloaded from www.hdotronic.com/whitepaper/fine-patt.pdf on March 12, 2002
Examiner 		Date Considered 12/19/02

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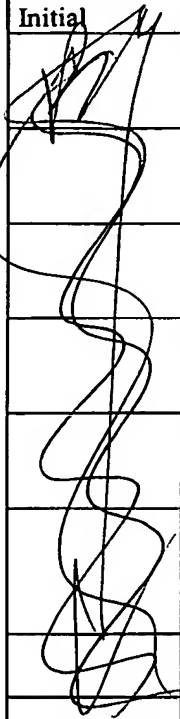
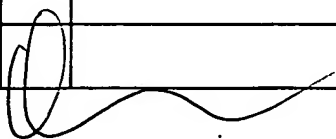
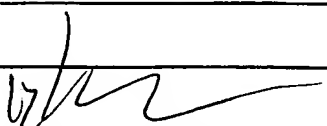
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Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
Bk	C29	Meli et al., "Self-Assembled Masks for the Transfer of Nanometer-Scale Patterns into Surfaces: Characterization by AFM and LFM", Nano Letters, Vol. 2, No. 2, 2002, 131-135
	C30	"Shipley Claims Porous Low K Dielectric Breakthrough," Press Release March 17, 2003.
	C31	Jeffrey M. Calvert and Michael K. Gallagher, Semiconductor International, 26 (12), 56 (2003).
	C32	Van Bavel et al., Future Fab International, 16, (2004).
	C33	Caluwaerts et al, "Post Patterning Meso Porosity Creation: A Potential Solution For Pore Sealing," IITC 2003.
V	C34	Peter Singer, "New Materials and Designs to Improve Transistor Performance", April 1, 2004, Semiconductor International.
	C35	Ghani et al, "A 90nm High Volume Manufacturing Logic Technology Featuring Novel 45nm Gate Length Strained Silicon CMOS Transistors", IEEE, © 2003.
	C36	Bhadri N. Varadarajan, "Tensile Silicon Nitride - P1264 NESL", C & F Study, August 21, 2003.
	C37	Varadarajan, et al., "Strained Transistor Architecture and Method", Novellus Systems, Inc., Appln No. 10/923,259, filed August 20, 2004, pages 1-24. [Atty Docket No. NOVLP108/NVLS-2933].
	C38	Niu et al., "Methods For Improving The Cracking Resistance Of Low-K Dielectric Materials", U.S. Application No. 10/860,340, filed June 2, 2004, (Atty Dkt: NOVLP099)
	C39	Niu et al., "Methods For Improving The Cracking Resistance Of Low-K Dielectric Materials", U.S. Application No. 10/860,340, Office Action dated March 2, 2005, (Atty Dkt: NOVLP099)
	C40	Niu et al., "Methods For Improving The Cracking Resistance Of Low-K Dielectric Materials", U.S. Application No. 10/860,340, Final Office Action dated June 13, 2005, (Atty Dkt: NOVLP099)
	C41	Wang et al., "Plasma Detemplating And Silanol Capping Of Porous Dielectric Films", U.S. Application No. 10/785,235, filed February 23, 2004 (Atty Dkt: NOVLP085)
	C42	Varadarajan et al., "Tensile Dielectric Films Using UV Curing", U.S. Application No. 10/972,084, filed October 22, 2004 (Atty Dkt: NOVLP122)
	C43	Fox et al., "Method For Improving Mechanical Properties Of Low Dielectric Constant Materials", U.S. Application No. 10/849,568, filed May 18, 2004 (Atty Dkt: NOVLP083)
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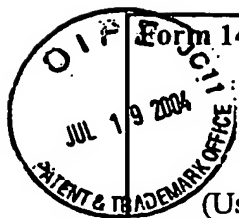
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Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	C44	Fox et al., "Methods For Producing Low-Stress Carbon-Doped Oxide Films With Improved Integration Properties", U.S. Application No. 10/987,208, filed November 12, 2004 (Atty Dkt: NOVLP104)
	C45	Van Den Hoek et al., "VLSI Fabrication Processes For Introducing Pores Into Dielectric Materials," U.S. Application No. 11/050,621, filed January 31, 2005 (Atty Dkt: NOVLP100)
	C46	Draeger et al., "Creation Of Porosity In Low-K Films By Photo-Disassociation Of Imbedded Nanoparticles," U.S. Application No. 11/146,456, filed June 6, 2005 (Atty Dkt: NOVLP100X1)
	C47	Wu et al., "Methods For Producing Low Stress Porous Low-K Dielectric Materials Using Precursors With Organic Functional Groups", U.S. Application No. 10/927,777, filed August 27, 2004 (Atty Dkt: NOVLP106)
	C48	Wu et al., "Methods For Improving Integration Performance Of Low Stress CDO Films", U.S. Application No. 10/941,502, filed September 14, 2004 (Atty Dkt: NOVLP107)
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	C50	Wu et al., U.S. Application No. 10/789,103 (Atty Docket No.: NOVLP094), entitled: Methods For Producing Low-K CDO Films With Low Residual Stress
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Form 1449 (Modified)

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Atty Docket No.

NOVLP094

Applicant:

Wu et al.

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Group

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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
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	C						
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Foreign Patent or Published Foreign Patent Application

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							Yes	No
	J							

Other Documents

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Wu	K	Jan, C.H., et al, 90NM Generation, 300mm Wafer Low k ILD/Cu Interconnect Technology, 2003 IEEE Interconnect Technology Conference.
	L	U.S. Application No. 10/820,525 (Atty Docket No.: NOVLP091), entitled: METHODS FOR PRODUCING LOW-K CDO FILMS WITH LOW RESIDUAL STRESS, Wu et al. NOT A PUBLICATION
	M	
Examiner		Date Considered
Wu		12/19/05

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